

In the Claims:

Please amend the claims as follows:

Claims 1-24 (Canceled)

Claim 25 (New): An electronic device comprising:

a plurality of source signal lines;

a plurality of gate signal lines;

a plurality of electric current supply lines;

a plurality of reset signal lines; and

a plurality of pixels, each of the plurality of pixels comprising a switching transistor, an EL driver transistor and a reset transistor,

wherein:

a gate electrode of the switching transistor is electrically connected to one of the plurality of gate signal lines;

one of a source region and a drain region of the switching transistor is electrically connected to one of the plurality of source signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

a gate electrode of the reset transistor is electrically connected to one of the plurality of reset signal lines;

one of a source region and a drain region of the reset transistor is electrically connected to one of the plurality of gate signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

one of a source region and a drain region of the EL driver transistor is electrically connected to one of the plurality of electric current supply lines.

Claim 26 (New): An electronic device comprising:

a plurality of source signal lines;

a plurality of gate signal lines;

a plurality of electric current supply lines;
a plurality of reset signal lines; and
a plurality of pixels, each of the plurality of pixels comprising a switching transistor, an EL driver transistor, a reset transistor and an EL element,

wherein:

a gate electrode of the switching transistor is electrically connected to one of the plurality of gate signal lines;

one of a source region and a drain region of the switching transistor is electrically connected to one of the plurality of source signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

a gate electrode of the reset transistor is electrically connected to one of the plurality of reset signal lines;

one of a source region and a drain region of the reset transistor is electrically connected to one of the plurality of gate signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor; and

one of a source region and a drain region of the EL driver transistor is electrically connected to one of the plurality of electric current supply lines, and the remaining one of the source region and the drain region is electrically connected to one electrode of the EL element.

Claim 27 (New): An electronic device comprising:

a plurality of source signal lines;
a plurality of gate signal lines;
a plurality of electric current supply lines;
a plurality of reset signal lines; and
a plurality of pixels, each of the plurality of pixels comprising a switching transistor, an EL driver transistor, a reset transistor and an EL element,

wherein:

a gate electrode of the switching transistor is electrically connected to one of the plurality of gate signal lines;

one of a source region and a drain region of the switching transistor is electrically connected to one of the plurality of source signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

a gate electrode of the reset transistor is electrically connected to one of the plurality of reset signal lines;

one of a source region and a drain region of the reset transistor is electrically connected to one of the plurality of gate signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

one of a source region and a drain region of the EL driver transistor is electrically connected to one of the plurality of electric current supply lines, and the remaining one of the source region and the drain region is electrically connected to one electrode of the EL element.

Claim 28 (New): An electronic device comprising:

a plurality of source signal lines;

a plurality of gate signal lines;

a plurality of electric current supply lines;

a plurality of reset signal lines; and

a plurality of pixels, each of the plurality of pixels comprising a switching transistor, an EL driver transistor, a reset transistor, a storage capacitor and an EL element,

wherein:

a gate electrode of the switching transistor is electrically connected to one of the plurality of gate signal lines;

one of a source region and a drain region of the switching transistor is electrically connected to one of the plurality of source signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

a gate electrode of the reset transistor is electrically connected to one of the plurality of reset signal lines;

one of a source region and a drain region of the reset transistor is electrically connected to one of the plurality of gate signal lines, and the remaining one of the source region and the drain region is electrically connected to a gate electrode of the EL driver transistor;

one electrode of the storage capacitor is electrically connected to one of the plurality of electric current supply lines, and the remaining electrode is electrically connected to the gate electrode of the EL driver transistor; and

one of a source region and a drain region of the EL driver transistor is electrically connected to one of the plurality of electric current supply lines, and the remaining one of the source region and the drain region is electrically connected to one electrode of the EL element.

Claim 29 (New): An electronic device according to claim 25 further comprising a source signal line driver circuit, a gate signal line driver circuit and a reset signal line driver circuit.

Claim 30 (New): An electronic device according to claim 26 further comprising a source signal line driver circuit, a gate signal line driver circuit and a reset signal line driver circuit.

Claim 31 (New): An electronic device according to claim 27 further comprising a source signal line driver circuit, a gate signal line driver circuit and a reset signal line driver circuit.

Claim 32 (New): An electronic device according to claim 28 further comprising a source signal line driver circuit, a gate signal line driver circuit and a reset signal line driver circuit.

Claim 33 (New): An electronic device according to claim 25, wherein said electronic device is a device selected from the group consisting of: an EL display, a video camera, a head mounted display, a DVD player, a personal computer, a portable telephone and a car audio system.

Claim 34 (New): An electronic device according to claim 26, wherein said electronic device is a device selected from the group consisting of: an EL display, a video camera, a head

mounted display, a DVD player, a personal computer, a portable telephone and a car audio system.

Claim 35 (New): An electronic device according to claim 27, wherein said electronic device is a device selected from the group consisting of: an EL display, a video camera, a head mounted display, a DVD player, a personal computer, a portable telephone and a car audio system.

Claim 36 (New): An electronic device according to claim 28, wherein said electronic device is a device selected from the group consisting of: an EL display, a video camera, a head mounted display, a DVD player, a personal computer, a portable telephone and a car audio system.

Claim 37 (New): An electronic device according to claim 27, wherein:
a p-channel polarity transistor is used for the switching transistor when the source region or the drain region of the EL driver transistor is electrically connected to an anode of the EL element; and
an n-channel polarity transistor is used for the switching transistor when the source region or the drain region of the EL driver transistor is electrically connected to a cathode of the EL element.

Claim 38 (New): An electronic device according to claim 28, wherein:
a p-channel polarity transistor is used for the switching transistor when the source region or the drain region of the EL, driver transistor is electrically connected to an anode of the EL element; and
an n-channel polarity transistor is used for the switching transistor when the source region or the drain region of the EL driver transistor is electrically connected to a cathode of the EL element.